

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A processor operable in a plurality of modes, and a plurality of domains, said plurality of domains comprising a first domain and a second domain, the processor comprising:

monitoring logic configured to perform a debug or trace function to monitor said processor and to capture diagnostic data in response to performing said debug or trace function;

a storage element configured to store at least one control parameter;

control logic configured to control said monitoring logic in dependence on said at least one control parameter and the domain in which said processor is operating to suppress capturing of diagnostic data relating to predetermined activities of said processor in said first domain while allowing capturing of diagnostic data relating to predetermined activities of said processor in said second domain.

2. (Previously Presented) A processor according to claim 1, wherein the first domain is a secure domain and the second domain is a non-secure domain, said processor being configured such that when executing a program in a secure mode within said secure domain said program has access to secure data which is not accessible when said processor is operating in a non-secure mode within said non-secure domain.

3. (Previously Presented) A processor according to claim 1, wherein the at least one control parameter provides an indication of said domain of operation of the processor, said control logic being configured to suppress capturing of diagnostic data when said processor switches from second to first domain.

4. (Previously Presented) A processor according to claim 1, wherein said at least one control parameter identifies an application, said control logic being configured to suppress capturing of diagnostic data when said processor switches from an identified application in said

first domain to an application in said first domain not identified by said at least one control parameter.

5. (Previously Presented) A processor according to claim 1, wherein said first domain comprises a plurality of modes and said at least one control parameter identifies a particular mode within said first domain, said control logic being configured to suppress capturing of diagnostic data when said processor switches between an identified mode within said first domain and a mode within said first domain not identified by said at least one control parameter.

6. (Original) A processor according to claim 5, wherein said plurality of modes in said first domain comprise a user mode and a privileged mode.

7. (Previously Presented) A processor according to claim 1, wherein said control logic is configured to control said monitoring logic to resume capturing of diagnostic data when said processor switches back from said predetermined activity to an activity for which capturing of diagnostic data is not suppressed.

8. Canceled.

9. Canceled.

10. (Original) A processor according to claim 1, wherein said control logic suppresses capture of said diagnostic data by removing power input to the monitoring logic.

11. (Currently Amended) A method of controlling a debug or trace function for monitoring a processor and capturing diagnostic data from said processor, said processor being operable in a plurality of modes and a plurality of domains, said plurality of domains comprising a first domain and a second domain, the method comprising:

setting a control parameter indicative of whether capturing of diagnostic data is allowable;

monitoring said processor to capture diagnostic data;

suppressing capturing of diagnostic data relating to predetermined activities of said processor in said first domain while allowing capturing of diagnostic data relating to predetermined activities of said processor in said second domain in dependence on said control parameter.

12. (Original) A method according to claim 11, wherein said first domain comprises a secure domain and said second domain comprises a non-secure domain, said processor being operable such that when executing a program in a secure mode within said secure domain said program has access to secure data which is not accessible when said processor is operating in a non-secure mode within said non-secure domain.

13. (Original) A method according to claim 11, wherein said at least one control parameter provides an indication of the domain of operation of said processor, data capture being suppressed when said processor switches operation from second to first domain.

14. (Original) A method according to claim 11, wherein said at least one control parameter identifies an application, diagnostic data capture being suppressed when operation of said processor switches from an identified application in said first domain to an application in said first domain not identified by said at least one control parameter.

15. (Original) A method according to claim 11, wherein said first domain comprises a plurality of modes and said at least one control parameter identifies a particular mode within said first domain, capturing of diagnostic data being suppressed when said processor switches between an identified mode within said first domain and a mode within said first domain not identified by said at least one control parameter.

16. (Original) A method according to claim 15, wherein said plurality of modes in said first domain comprise a user mode and a privileged mode.

17. (Original) A method according to claim 11, comprising the further step of:
resuming capturing of diagnostic data when said processor switches back from said predetermined activity to an activity for which capturing of diagnostic data is not suppressed.

18. Canceled.

19. Canceled.